FORM 1449*	INFORMATION DISCLOSURE STATEMENT	Docket Number: NL030979US1	Application Number: 10/568,013
	IN AN APPLICATION	Applicant: GANGWAL et al.	
	(Use several sheets if necessary)	Filing Date: 02/10/2006	Group Art Unit: 2181

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,446,191	09/03/2002	Pechanek *et al.*			
	2001/0054126	12/20/2001	Fukuda *et al.*			
	5,729,758	03/17/1998	Inoue *et al.*			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	EP 0 638 868 A2	02/15/1995	EPO			English	
	DE 101 59 283 A1	06/12/2003	Germany (DE)			In German-with English Abstract	
	JP 61-231657 A	10/15/1986	Japan			In Japanese with English Abstract	
	JP 8-30577 A and attached corresponding US 5,729,758	02/02/1996 US Date 3/17/1998	Japan US			In Japanese with English Abstract and corresponding US patent attached (US 5,729,758)	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	1997	Dahle *et al.* "Kestrel: Design of an 8-bit SIMD parallel processor." Proc. 17th Conf. on Advanced Research in VSLI, Sept. 15-17, 1997, 18 pgs.
	1994	Okazaki *et al.* "Integrated Memory Array Processor and Real-Time Vision System for Vehicle Control." Vehicle Navigation and Information Systems Conference, Aug. 31-Sept. 2, 1994, pp. 103-106.

EXAMINER	/William Treat/	DATE CONSIDERED	12/14/2009

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /WT/